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10/798,994	03/11/2004	Jeffery Steven Beck	APTI-160US	6851

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EXAMINER	
NGUYEN, LUONG TRUNG	
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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

DETAILED ACTION

Response to Arguments

1. Applicant's arguments filed on 09/16/2010 have been fully considered but they are not persuasive.

In re pages 7-8, Applicants argue that Morse does not disclose or suggest that each video amplifier includes a single capacitor having a terminal switched between a respective column input and an output of the video amplifier, as required by claim 1. Morse is silent regarding this feature. Contrary to the recitation in claim 1, Morse requires the use of two capacitors (coupling capacitor 14 and integrating capacitor 18). Thus, the terminal of coupling capacitor 14 (at node 20) cannot be switched between a respective column input and an output of the video amplifier, as required by claim 1. Accordingly, Morse does not make up for the deficiencies of Borg and Henderson with respect to claim 1.

In response, regarding claim 1, the Applicants recited limitation "each video amplifier includes a single capacitor having a terminal switched between a respective column input and an output of the video amplifier." The Examiner considers that claim 1 as recited still does not distinguish from Borg et al. and Henderson in view of Morse et al. Borg et al. and Henderson do not disclose this limitation; however, Morse et al. disclose this limitation.

Morse et al. teaches an amplifier circuit 12, which comprises a coupling capacitor 14 and a capacitor reset switch 24; when the capacitor reset switch 24 is closed in a first time period, the terminal of capacitor 14 at node 20 is connected to the output 22 of the amplifier 12, i.e., the signal charge at the terminal of capacitor 14 at node 20 is transmitted to the output node 22 of

amplifier 12 via closed switch 24 (figure 3, column 2, line 53 - column 3, line 25), when the capacitor reset switch 24 is opened in a second time period, the terminal of capacitor 14 at node 20 is coupled to the input node of the amplifier 12, i.e., the signal charge at the terminal of capacitor 14 at node 20 is transmitted to the input of node of the amplifier 12 (figure 3, column 2, line 53 - column 3, line 25). This broadly reads on limitation "each video amplifier includes a single capacitor having a terminal switched between a respective column input and an output of the video amplifier."

2. Any inquiry concerning this communication or earlier communications from the examiner should be directed to LUONG T. NGUYEN whose telephone number is (571)272-7315. The examiner can normally be reached on 7:30AM - 5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, DAVID L. OMETZ can be reached on (571) 272-7593. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/LUONG T NGUYEN/
Primary Examiner, Art Unit 2622
10/05/10